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Digital Logic Design 01

Q. 5.9: A sequential circuit has two JK flip-flops A and B and one input x. The circuit is described

~~Q. 4.14: Assume that the exclusive-OR gate has a propagation delay of 10 ns and that the AND or OR~~

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Q. 3.9: Find all the prime implicants for the following Boolean functions, and determine which are Digital Design Mano 2nd Edition

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