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Introduction to RTL

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Using VHDL What is

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In digital circuit  
design, register-

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### in hardware description

languages like Verilog and VHDL to create high-level representations of a circuit, from which lower-level representations and ultimately actual wiring can be derived. Design at the RTL level

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### ~~Register transfer~~ ~~level - Wikipedia~~

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combinational,  
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Vahid: Edition: 2,  
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~~VHDL simulation  
expert needed |  
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Instead, a behavioral description is developed to

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### Analyze the design

in terms of functionality, performance, and other high-level issues using a Hardware Description Language such as Verilog or VHDL. This is usually done by a digital designer and is similar to a high-

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