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Q. 4.1: Consider the combinational circuit shown in Fig. P4.1.(a)* Derive the Boolean expressions for

1.6: The solutions to the quadratic equation $x^2 - 11x + 22 = 0$ are $x = 3$ and $x = 6$. *How to start your own business?*

Q. 5.1:

The D latch of Fig. 5.6 is constructed with four NAND gates and an inverter. Consider the

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1. Introduction
2. Combinational Logic Design
3. Sequential Logic Design-Controllers
4. Datapath Components
5. Register-Transfer Level (RTL) Design
6. Optimizations and Tradeoffs
7. Physical Implementation
8. Programmable Processors
9. Hardware Description Languages

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This book introduces a modern approach to embedded system design, presenting software design and hardware design in a unified manner. It covers trends and challenges, introduces the design and use of single-purpose processors ("hardware") and general-purpose processors ("software"), describes memories and buses, illustrates hardware/software tradeoffs using a digital camera example, and discusses advanced computation models, controls systems, chip technologies, and modern design tools. For courses found in EE, CS and other engineering departments.

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books present a perspective rooted in the 1970s and 1980s, Digital System Design takes the subject into the 21st century. It quickly moves through the low-levels of design, making a clear distinction between design and gate-level minimization. The book also emphasizes how one of the key uses of digital design today is to build high-performance alternatives to software in addition to glue logic. And it swiftly progresses to register-transfer-level (RTL) design since that is the level at which most digital design in practice today is performed.

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digital systems design. The text's focus on register-transfer-level design and present-day applications not only leads to a better appreciation of computers and of today's ubiquitous digital devices, but also provides for a better understanding of careers involving digital design and embedded system design. The book's key features include: An emphasis on register-transfer-level (RTL) design, the level at which most digital design is practiced today, giving readers a modern perspective of the field's applicability. Yet, coverage stays bottom-up and concrete, starting from basic transistors and gates, and moving step-by-step up to more complex components. Extensive use of basic examples

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to teach and illustrate new concepts, and of application examples, such as pacemakers, ultrasound machines, automobiles, and cell phones, to demonstrate the immediate relevance of the concepts. Separation of basic design from optimization, allowing development of a solid understanding of basic design, before considering the more advanced topic of optimization. Flexible organization, enabling early or late coverage of optimization methods or of HDLs, and enabling choice of VHDL, Verilog, or SystemC HDLs. Career insights and advice from designers with varying levels of experience. A clear bottom-up description of field-programmable

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gate arrays (FPGAs). About the Author: Frank Vahid is a Professor of Computer Science & Engineering at the University of California, Riverside. He holds Electrical Engineering and Computer Science degrees; has worked/consulted for Hewlett Packard, AMCC, NEC, Motorola, and medical equipment makers; holds 3 U.S. patents; has received several teaching awards; helped setup UCR's Computer Engineering program; has authored two previous textbooks; and has published over 120 papers on digital design topics (automation, architecture, and low-power).

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and construct a radio signal to send it from your cell phone to a base station. They command robots on a factory floor, power generation in a power plant, processes in a chemical plant, and traffic lights in a city. These less visible computers are called embedded systems, and the software they run is called embedded software. The principal challenges in designing and analyzing embedded systems stem from their interaction with physical processes. This book takes a cyber-physical approach to embedded systems, introducing the engineering concepts underlying embedded systems as a technology and as a subject of study. The focus is on modeling, design, and analysis of

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cyber-physical systems, which integrate computation, networking, and physical processes. The second edition offers two new chapters, several new exercises, and other improvements. The book can be used as a textbook at the advanced undergraduate or introductory graduate level and as a professional reference for practicing engineers and computer scientists. Readers should have some familiarity with machine structures, computer programming, basic discrete mathematics and algorithms, and signals and systems.

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books, this RISC-V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC-V microprocessor.

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the basis for designing a RISC-V processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use SparkFun's RED-V RedBoard to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic

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design and reinforces logic concepts through the design of a RISC-V microprocessor Gives students a full understanding of the RISC-V instruction set architecture, enabling them to build a RISC-V processor and program the RISC-V processor in hardware simulation, software simulation, and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single-cycle, multicycle, and pipelined versions of the RISC-V architecture Features a companion website with a bonus chapter on I/O systems with practical examples that show how to use SparkFun's RED-V RedBoard to communicate with peripheral devices such as LCDs,

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