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Many power management techniques, including multi-voltage power shutdown, can add significantly higher complexity to the design because it actually shuts down part of the operation of a design,\" said ...

Lower Power Chips: What To Watch Out For
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Engineering.com Launches New TV Video Platform to Further Enhance Engagement and the User Experience
Arm's Simon Tatham introduces an open-source code base for analyzing and browsing trace files in Tarmac trace format top make it easier to get a look at exactly how a CPU is behaving. The ESD Alliance ...

Blog Review: July 14
Semtech's laser drivers and amplifiers are integrated into Intel RealSense LiDAR Camera L515 to create a power-efficient, high-resolution consumer solution. We talk to Simon McCaul, Senior Market ...

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In this seventh video, the panel looks at some ideas to define a new curvilinear data format to reduce file sizes. Aki Fujimura of D2S opens the panel discussion with an explanation of the problem for ...

Developing A New Curvilinear Data Format
Dave Didio Miami Herald file Investigators from around the world ... Nathan Denette AP According to a report in engineering.com: \"The roof collapsed when one car too many parked on the rooftop ...

Have buildings spontaneously collapsed before? Yes, but it's a short list
Higher resolutions produce more image data and generate larger file sizes. Whether that data comes from inspection, metrology, or even various test processes, some of it has to be stored and made ...

IC Data Hot Potato: Who Owns And Manages It?
As such, symbolic simulation is commonly used to verify memories which typically have low sequential depth such as SRAMs, multi-port register files, CAMs and ROMs. Image 2: Symbolic Simulation ...

Cell Library Verification Using Symbolic Simulation
SDCs can specify clocks, timing, setup and hold checks, MCPs, FPs and other useful information in a single file to guide logic synthesis, static timing analysis and other steps in the development flow ...

Early Simulation Of Multi-Cycle Paths And False Paths
The system leverages existing 3D sensing technologies that use light to measure every feature of a building's interior, room by room and floor by floor, to create a massive data file that captures the ...

System Bits: July 5
So even just identifying what is a piece of work, what data is actually pulling from, which file systems are tools retrieving the data from, what is a particular task using – there are a lot of ...

Rocky Road To Designing Chips In The Cloud
Virtual platforms, high-level floorplanners, and power intent files are just a few examples of this trend in action. VC Functional Safety Manager has recently made early, high-level, top-down FMEDA ...

Making Autonomous Driver Chips Safe From The Top Down
JEDEC established JEP181, a neutral file, XML-based standard aimed at simplifying thermal model data sharing between suppliers and end-users in a single file format called ECXML (Electronics Cooling ...

Week In Review: Design, Low Power
Each team cooks their own or adapts another team's code. They do the same for memory maps, header files, and software macros – more script-ware. Product managers want to keep product documentation in ...

IP-XACT Is Back, For All The Right Reasons
Modeling in a standard file format like LVS is not accurate enough. It's not silicon-accurate. Second, the impact of voltage and timing at near-threshold compute is significant.\" Simply put, power is ...

Near-Threshold Issues Deepen
We talk to Ted Pawela of Altium about the Nexar Cloud platform, which can build a connected ecosystem of design, supply chain, and manufacturing stakeholders, enhancing the engineering of smart & ...

Leveraging the Cloud To Connect the Electronics Industry Ecosystem
Calibra's Jan Willis looks at the tradeoffs involved in defining a new curvilinear data format to reduce file sizes. SEMI's Sungho Yoon urges wafer suppliers to boost capacity as market demand and ...