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The Field Programmable Gate Array (FPGA) code was based on C++.

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be obtained by using FPGA-based accelerators at expenses of a longer development time. I. INTRODUCTION In recent years financial applications have gained a significant portion of the computer market as the number of financial computations and transactions over the network is increasing everyday. This large number of financial data is often to

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FPGA resources can be connected together to build your own custom hardware design to meet your specific needs. The Intel® Acceleration Stack for FPGAs is a new architecture that allows software developers to harness FPGAs much easier. Amongst the many uncertainties on the financial sector horizon, there are two things we know for sure.

FPGA in Finance - Intel
accelerate financial derivative calculations is becoming very common. In this work, we implement an FPGA-based specific processor for European option pricing using Monte Carlo simulations, and we compare its performance and power dissipation to the execution on a CPU. The experimental results show that

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