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"Fundamentals of Digital Logic with VHDL Design" teaches the basic design techniques for logic circuits. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples, which are easy to understand.

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~~Fundamentals of Digital Logic With Verilog Design~~ is intended for an introductory course in digital logic design. The main goals are (1) to teach students the fundamental concepts in classical manual digital design, and (2) illustrate clearly the way in which digital circuits are designed today, using CAD tools. Use of CAD software is well integrated into the book.

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~~Fundamentals of digital logic with Verilog design / Stephen D. Brown, Zvonko G. Vranesic.—1st~~

ed. p. cm. (McGraw-Hill Series in electrical and computer engineering) Includes index. ISBN 0-07-282315-1 1. Logic circuits—Design and construction—Data processing. 2. Verilog (Computer hardware description language). 3. Computer-aided design. I.

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Apply labels and defining design parameters, then use such labels to design basic props, applying the additive and subtractive transformations. These all tie into the core concepts of shape design. The demonstration and assignment covers shape design from cues students will be tasked with creating 3 pages of designs using based off primitive shapes.

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