

Influence Of Temperature On Microelectronics And System Reliability A Physics Of Failure Approach Electronic Packaging

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The temperature effects on electrical parameters of both bipolar and MOSFET devices are discussed, and models quantifying the temperature effects on package elements are identified. Temperature-related models have been used to derive derating criteria for determining the maximum and minimum allowable temperature stresses for a given microelectronic package architecture.

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Features Presents the effect of temperature in the context of microelectronics reliability, covering damage mechanisms in the temperature range of -55 ° C to 150 ° C. Uses the cumulative effect of competing failure processes on device life to determine appropriate values of operating temperature and non-temperature related stress.

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It has negative temperature co-efficient of resistance. That means the resistance of a semiconductor decreases with increase in temperature and vice-versa. When a suitable metallic impurity is added to a semiconductor, its current

conducting properties change appreciably. Commonly Used Semiconductors

~~What is a Semiconductor? Discuss the effect of temperature...~~

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