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37. The internal cache is loaded with the base address, offset address, and access rights byte

39. The GDTR address the Global Descriptor Table 41.4,096 43. 4M 45. 30000000H 49.

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Barry Brey is one of America's Premier authors whose many books on microprocessors and assembly language, based on his actual teaching experience, have helped educate hundreds of thousands of students and readers throughout North America and the world. My wife and children say I spend too much at writing. I am married to a beautiful lady (Sheila) and I have one child from a prior marriage (Brenda, who is a graduate of DeVry, with a computer

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inline assembler. Updated sections that detail new events in the fields of microprocessors and microprocessor interfacing have been added. Organized in an orderly and manageable format, this text offers more than 200 programming examples using the Microsoft Macro Assembler program and provides a thorough description of each of the Intel family members, memory systems, and various I/O systems.

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inline assembler. Updated sections that detail new events in the fields of microprocessors and microprocessor interfacing have been added. Organized in an orderly and manageable format, this text offers more than 200 programming examples using the Microsoft Macro Assembler program and provides a thorough description of each of the Intel family members, memory systems, and various I/O systems.

Presents programming, interfacing and applications for the 80286, 80386 and 80486 Intel microprocessors. This text is organized into two parts - the microprocessor as a programmable device and the microprocessor within its environment.

"Intel microprocessors have gained wide application in many areas of electronic communications, control systems, and desktop computer systems. This practical text is written for anyone who requires or desires a thorough knowledge of microprocessor programming and interfacing."-back cover.

Coverage first concentrates on real-mode assembly language programming compatible with all versions of the Intel microprocessor family, and compares and contrasts advanced family member with the foundational 8086/8088. This building block presentation is effective because the Intel family units are so similar that learning advanced versions is easy once the basics are understood.

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The new second edition presents the fundamental software and hardware needed to begin understanding the 8-bit chip. Coverage prepares readers for all aspects of microprocessors, beginning with the necessary 8-bit chip format and concluding with the faster 16-bit and 32-bit chips, including new coverage of parallel and serial data, an overview of the 8086/8088 family of microprocessors, and many more programming examples.

This fourth edition of "The Intel Microprocessors 8086/8088, 80186, 80286, 80386, 80486, Pentium, and Pentium Pro Processor: Architecture, Programming, and Interfacing" is a practical book for anyone interested in all programming and interfacing aspects of this important microprocessor family.

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