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Abstract Simulation of large-scale networks of spiking neurons has become appealing for understanding the computational principles of the nervous system by producing models based on biological evidence. In particular, networks that can assume a

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Real Time On-Chip Implementation of Dynamical Systems with Spiking Neurons WCCI 2012 IEEE World Congress on Computational Intelligence June, 10-15, 2012 - Brisbane, Australia IJCNN Real Time On-Chip Implementation of Dynamical Systems with Spiking Neurons Francesco Galluppi, Sergio Davies and Steve Furber

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Elon Musk to unveil brain chip with real-time neuron demonstration this week. Aug 25, 2020 | Featured, Science & Technology, Top Stories. Elon Musk's brain-machine interface company, Neuralink, has an event scheduled for later this week to update the public on its progress since last year's presentation. While the agenda is speculative for the most part, one expectation is a live demonstration of neuron activity.

Elon Musk to unveil brain chip with real-time neuron ...

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A real-time chip implementation for adaptive video coding control Abstract: The paper presents an adaptive coding control for real-time video coding systems. Based on temporal correlations, the group-of-pictures (GOP) of a video sequence is split into one basic GOP (BGOP) and many adaptive GOPs (AGOPs) and then processed accordingly.

A real-time chip implementation for adaptive video coding ...

Abstract: Single-pixel imaging (SPI) uses a single-pixel detector to create an image of an object. SPI relies on a computer to construct an image, thus increasing both the size and cost of SPI and limiting its application. We developed instant single-pixel imaging (ISPI), an on-chip SPI system that implements real-time imaging at a rate of 25 fps. ISPI uses the instant ghost imaging algorithm we proposed which leverages signal differences for image creation.

Instant single-pixel imaging: on-chip real-time ...

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The software Real Time Clock (RTC) is implemented using an 8-bit Timer/Counter with overflow interrupts enabled. Each timer overflow interrupt triggers an update of the software variables second, minute, hour, date, month, and year at the correct intervals. Figure 1-1. Oscillator Connection for Real Time Clock

AVR134: Real Time Clock (RTC) Using the Asynchronous Timer

The article presents the concept of real-time implementation computing tasks in video surveillance systems. A pipeline implementation of a multimodal background generation algorithm for colour video stream and a moving objects segmentation based on brightness, colour and textural information in reconfigurable resources of FPGA device is described.

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This paper deals with the real-time implementation of SPIHT algorithm using DSP chip. In order to facilitate the implementation and improve the codec's performance, some relative issues are thoroughly discussed, such as the optimization of program structure to speed up the wavelet decomposition.

Real-time implementation of a new low-memory SPIHT image ...

Real-time System Implementation for Video Processing. Paper ID #15811. Real-time System Implementation for Video Processing. Dr. Wagdy H Mahmoud, University of the District of Columbia Wagdy H. Mahmoud is an Associate Professor of electrical engineering at the Electrical Engineering Department at UDC. Mahmoud is actively involved in research in the areas of reconfigurable logic, hard- ware/software co-design of a system on a chip using reconfigurable logic, application-specific integrated ...

Real-time System Implementation for Video Processing

EMV is a payment method based upon a technical standard for smart payment cards and for payment terminals and automated teller machines which can accept them. EMV originally stood for "Europay, Mastercard, and Visa", the three companies which created the standard. EMV cards are smart cards, also called chip cards, integrated circuit cards, or IC cards which store their data on integrated ...

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