

The Motorola Mc68000 Microprocessor Family Embly Language Interface Design And System Design

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D68000 - IP Core for Motorola's 68000 [m68k, MC68008, MC68010, MC68020]

Oral History of Gary Davidian, part 2 of 2The Motorola Mc68000 Microprocessor Family

The V68000 is a synthesizable VHDL (soft) core design which is object code compatible with Motorola's popular MC68000. The V68000 is intended to be used in system-on-a-chip applications constructed ..

This revision introduces the characteristics of the Motorola 68000 family of processors.

This important revision introduces both students and practicing computer professionals to the characteristics of the Motorola 68000 family of processors. It has been widely applauded in previous editions as a text that is practical, easy to read, and designed to educate readers on the concepts as well as applied theory. In addition to its use as a learning aid, the text serves as a valuable reference in which topics are organized according to function and importance for the design of programs, interfaces or systems. This Second Edition has been updated to cover the most recent, relevant advances and developments affecting the MC68000 family of microprocessors.

The Motorola MC68000 family of microprocessors is undoubtedly a revolutionary set of devices. The MC68000 is the first advanced 16-bit microprocessor with a 32-bit internal architecture and the first with 16-megabyte, nonsegment ed, direct memory addressing. The processor's six basic addressing modes are equivalent to 14, when one considers all of the variations among these modes. Combined with the device's data and instruction types, the modes provide more than 1000 useful instructions. The book you are about to study has been developed as an aid to the hardware designer and as a supplement to the Motorola seminars on the 68000 microprocessor. The text includes a detailed description of the MC68000 and two complete systems that show how this processor can be interfaced to the outside world. The book follows a "top-down" approach. A brief history of microprocessors is provided first. Chapter 2 details the MC68000 by describing its registers, control lines, and capabilities. Chapter 3 introduces a small MC68000-based system. Although this system is characterized in the book as hypothetical, it is indeed the Educational Computer Board, used in the various Motorola seminars. The addressing modes and instructions are explained in Chapter 4, which includes helpful hints on how instructions can be used. Chapter 5 provides an in-depth description of additional instructions and numerous examples. Chapter 6 discusses exception handling and interrupts.

In the past several years, microprocessors have emerged as a major force in the computer industry, and the Motorola MC68000 family is regarded as an industry standard. The focus of this book is the Motorola MC68000 microprocessor family. Many of the design practices and fundamental concepts can apply to other modern microprocessors as well. This guide covers both the software and hardware of the M68000 family, and is designed as a text for a one-semester, junior-level microprocessor course that covers both programming and system design using the MC68000 microprocessor.

For one-semester, senior-level courses in Microprocessors, Assembly Language Programming and Microcomputer Design in departments of Electrical Engineering, Engineering Technology, Electronics Technology, and Computer Science. Designed to demystify the Motorola 68000 microprocessor its hardware and software this text leads students on an in-depth, hands-on exploration of more than 75 different applications and then guides them through the construction and programming of their own working single-board 68000 system.

*The introduction of the Motorola MC68000 family of microprocessors ushered in a new era of microprocessors. These are single-chip microprocessors designed to function as the central processing units of sophisticated computer systems. The prime objective of this thesis work is to develop a simulator for the MC68000 microprocessor mainly for educational purposes. The simulator would help in any test or research work utilizing 68000 assembly programs in the future. Most of the instructions in the 68000 family are implemented. Both the user mode and supervisory mode programs can be written and run against the simulator. Besides supporting most of the MC68000 features the simulator also has additional features to help debugging."--Abstract.

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