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IPC Users Guide. Inter-Processor Communication (IPC) provides a processor-agnostic API which can be used for communication between processors in a

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multi-processor environment (inter-core), communication to other threads on same processor (inter-process), and communication to peripherals (inter-device). The API supports message passing, streams, and linked

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lists.

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IPC is a component
containing packages that are
designed to allow
communication between

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processors in a multi-processor environment and communication to peripherals. This communication includes message passing, streams, and linked lists. These work transparently in both uni-

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processor and multi-processor configurations.

~~SYS/BIOS Inter-Processor
Communication (IPC) 1.25
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The IPC product contains an examples/archive directory

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with device-specific examples. Once identifying your device, the examples can be unzipped anywhere on your build host. Typically once unzipped, the user edits the example's individual products.mak file

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and simply invokes make.

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IPC can be used to
communicate with the
following: other threads on
the same processor. threads

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on other processors running
SYS/BIOS. threads on GPP
processors running a High-
Level Operating System
(HLOS) IPC was designed with
the needs of a wide variety
of users in mind.

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Introduction. This document
details Cdd Ipc module
implementations. Supported

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```
AUTOSAR Release : 4.3.1;  
Supported Configuration  
Variants : Pre-Compile; ...  
Received ping 9 Iteration 1  
from MCU 1 1 CDD_IPC_APP :  
Received ti.ipc4.ping-pong  
as ctrl MSG from MCU 2 0  
CDD_IPC_APP : Received ping
```

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0 Iteration 10 from MCU 2 0
CDD_IPC ...

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The `ti.sdo.ipc.Notify` module
manages the
multiplexing/demultiplexing

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of software interrupts over hardware interrupts. In order to use any Notify APIs, you must first call `Ipc_start()`. This sets up all the necessary Notify drivers, shared memory, and interprocessor interrupts.

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IPC provides an implementation of the IMessageQTransport interface called `ti.sdo.ipc.transports`

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.TransportShm (shared memory). You can write other implementations to meet your needs. When a transport is created via a transport-specific create() call, a remote processor ID (defined via the MultiProc module) is

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specified.

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Overview. IPC is a generic
term of Inter-Processor
Communication referred

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widely in the industry, but also a package in TI Processor SDK for multi-core communication. In generic usage, there are different ways for multi-core communication such as OpenCL, DCE, TI-IPC, and

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etc. In TI's IPC package, it uses a set of modules to facilitate the inter-processor communication.

~~3.7. IPC — Processor SDK
Linux Documentation~~

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Guide Ti Ipc User Guide This wiki-based user's guide is the new home of the Inter-Processor Communication (IPC) User's Guide (SPRUG06). That PDF-based document will no longer be updated. This user's guide

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contains the topics in the following list. It also links to API

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Guide for Powering DRA829V
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product provides software connectivity between multiple processors. Each processor may run either an HLOS (e.g. Linux, QNX) or an RTOS (e.g. SYS/BIOS). IPC is an open source project, currently managed with git,

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and maintained at
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together. Written by Texas Instruments' First DSP Educator Award winner, Naim Dahnoun, the book teaches readers how to use the development tools, take advantage of the maximum performance and

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functionality of this processor and have an understanding of the rich content which spans from architecture, development tools and programming models, such as OpenCL and OpenMP, to debugging tools.

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It also covers various multicore audio and image applications in detail. Additionally, this one-of-a-kind book is supplemented with: A rich set of tested laboratory exercises and solutions Audio and Image

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at all on the subject and its rich content with twenty chapters, *Multicore DSP: From Algorithms to Real-time Implementation on the TMS320C66x SoC* is a rare and much-needed source of information for

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undergraduates and postgraduates in the field that allows them to make real-time applications work in a relatively short period of time. It is also incredibly beneficial to hardware and software

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9th International Workshop on OpenMP, held in Canberra, Australia, in September 2013. The 14 technical full papers presented were carefully reviewed and selected from various submissions. The papers are

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